

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Toshihiko TANAKA et al.

Appln. No.:

Filed: Herewith

For: METHOD OF MANUFACTURING AN ELECTRONIC DEVICE AND A  
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants wish to make of record the documents cited  
in prior Application No. 09/983,172 filed October 23, 2001,  
whether cited by Applicants or by the Patent Office. The  
documents are listed on the attached Form PTO-1449.

Respectfully submitted,

MWS:jab

Miles & Stockbridge P.C.  
1751 Pinnacle Drive, Suite 500  
McLean, Virginia 22102-3833  
(703) 903-9000  
October 15, 2003

By: 

Mitchell W. Shapiro  
Reg. No. 31,568

<b>FORM PTO-1449</b>				Atty. Docket No. <b>XA-9566A</b>		Appln. No.	
<b><u>LIST OF DOCUMENTS CITED BY APPLICANT</u></b>							
				Applicant <b>Toshihiko TANAKA et al.</b>			
				Filing Date <b>HEREWITH</b>		Group	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AL	<b>9-211837</b>	<b>08/15/97</b>	<b>JAPAN</b>			<b>Abstract</b>
	AM	<b>5-289307</b>	<b>11/05/93</b>	<b>JAPAN</b>			<b>Abstract</b>
	AN	<b>5-181257</b>	<b>07/23/93</b>	<b>JAPAN</b>			<b>Abstract</b>
	AO						
	AP						
	AQ						
<b>OTHER</b> (including author, title, date, pertinent pages, etc.)							
	AR						
	AS						
	AT						
Examiner				Date Considered			
<b>EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							